

1/20

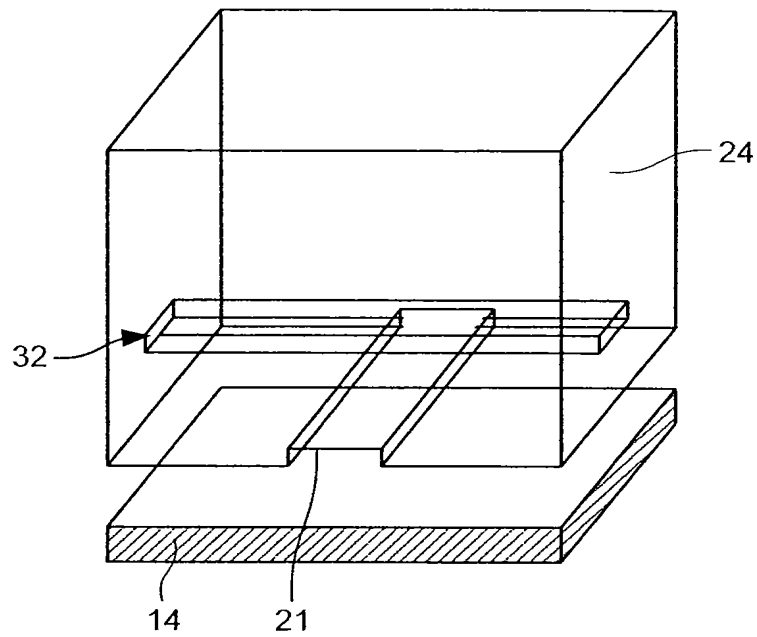


FIG. 1A

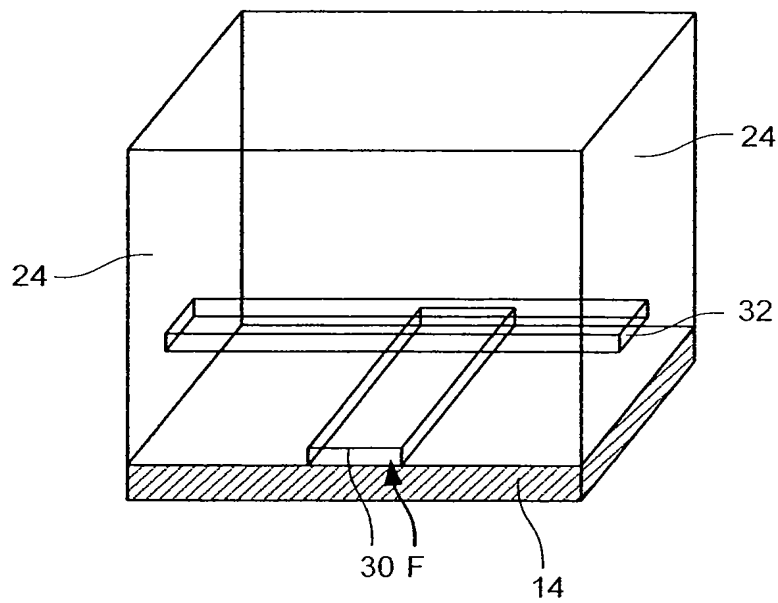


FIG. 1B

2/20

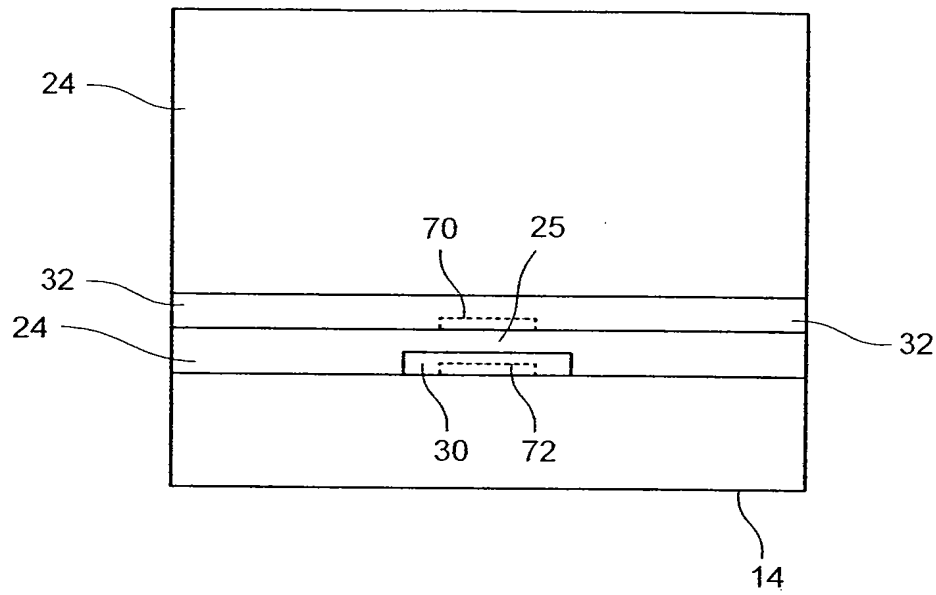


FIG. 2A

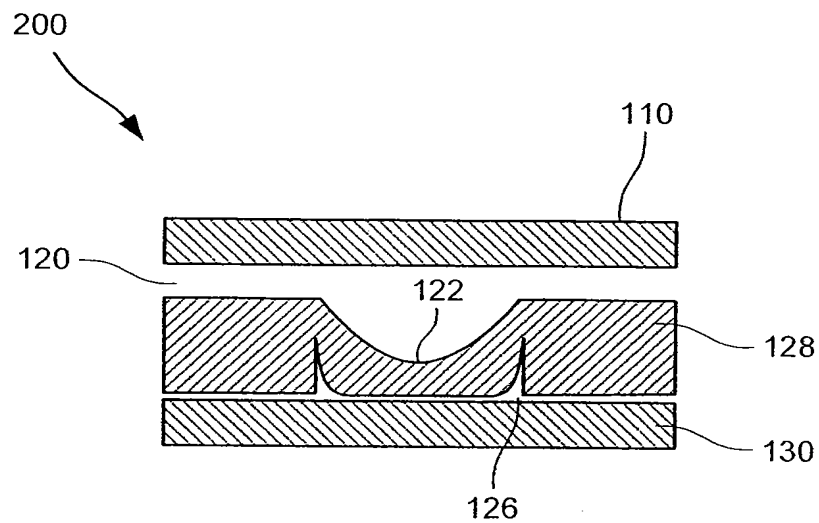


FIG. 2B

3/20

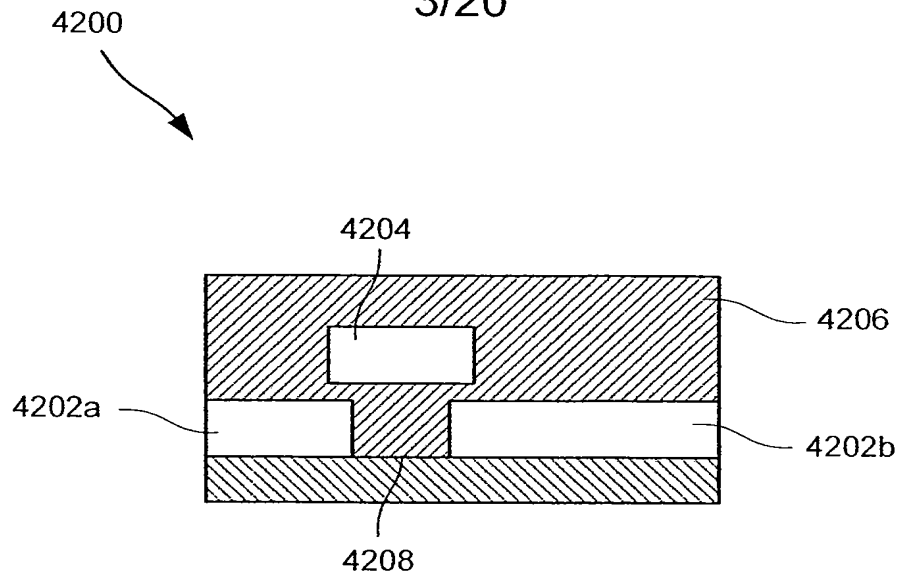


FIG. 3A

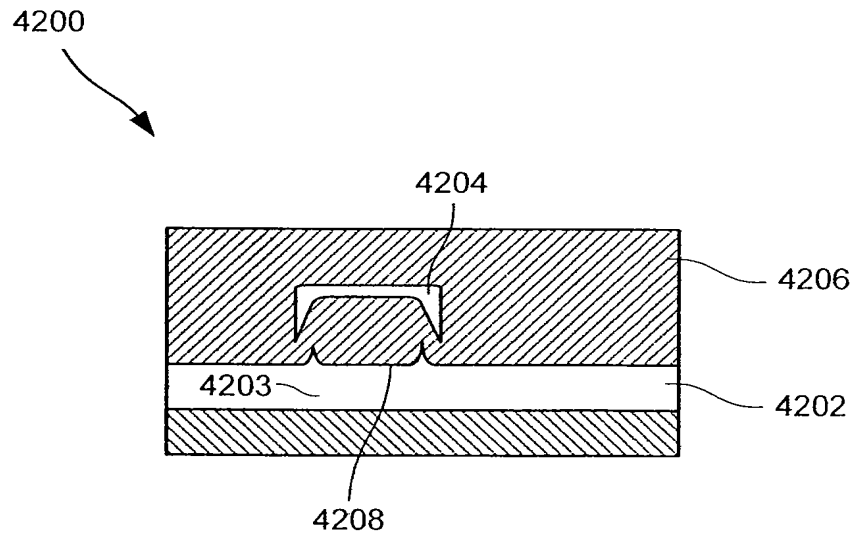


FIG. 3B

4/20

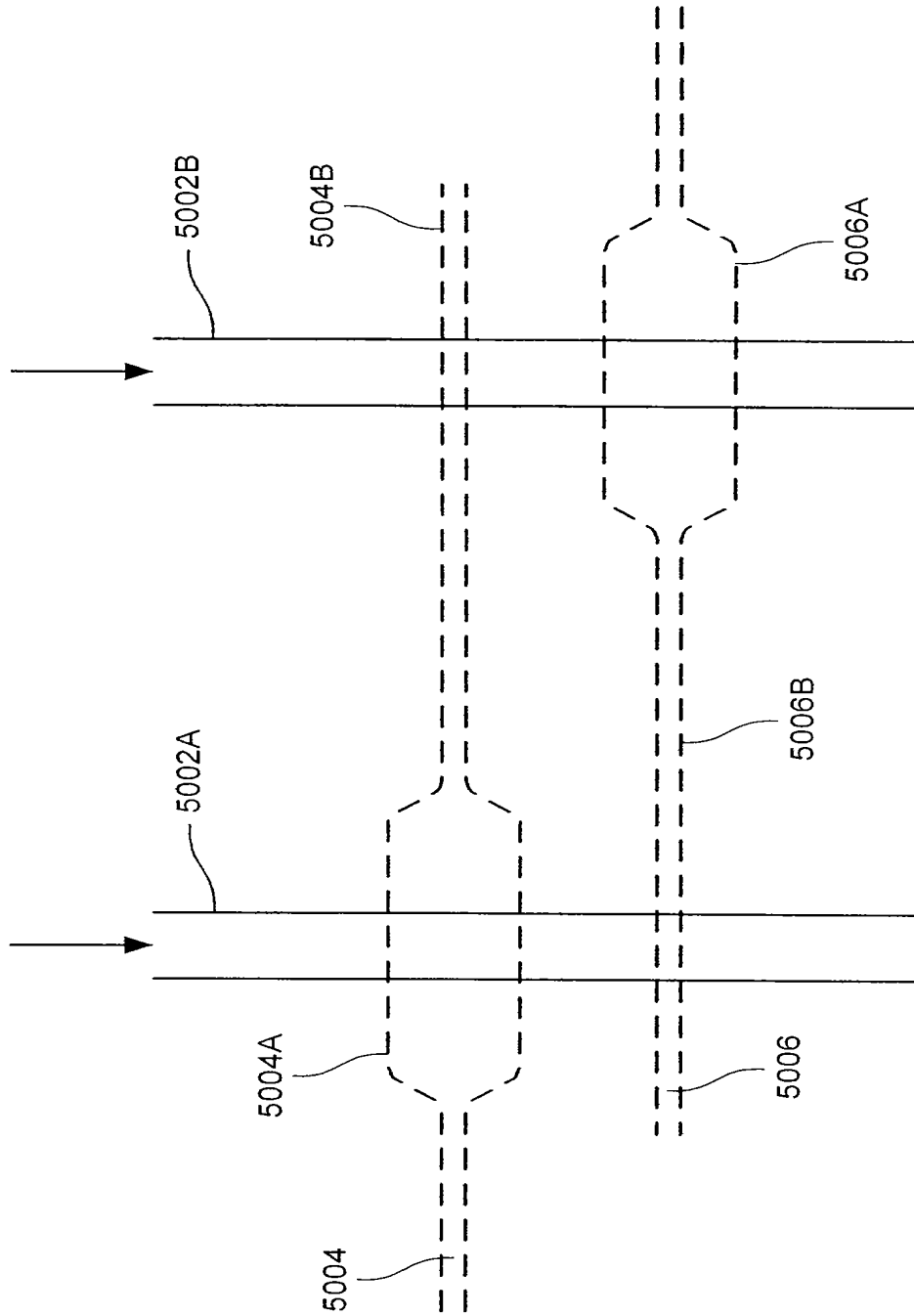


FIG. 4

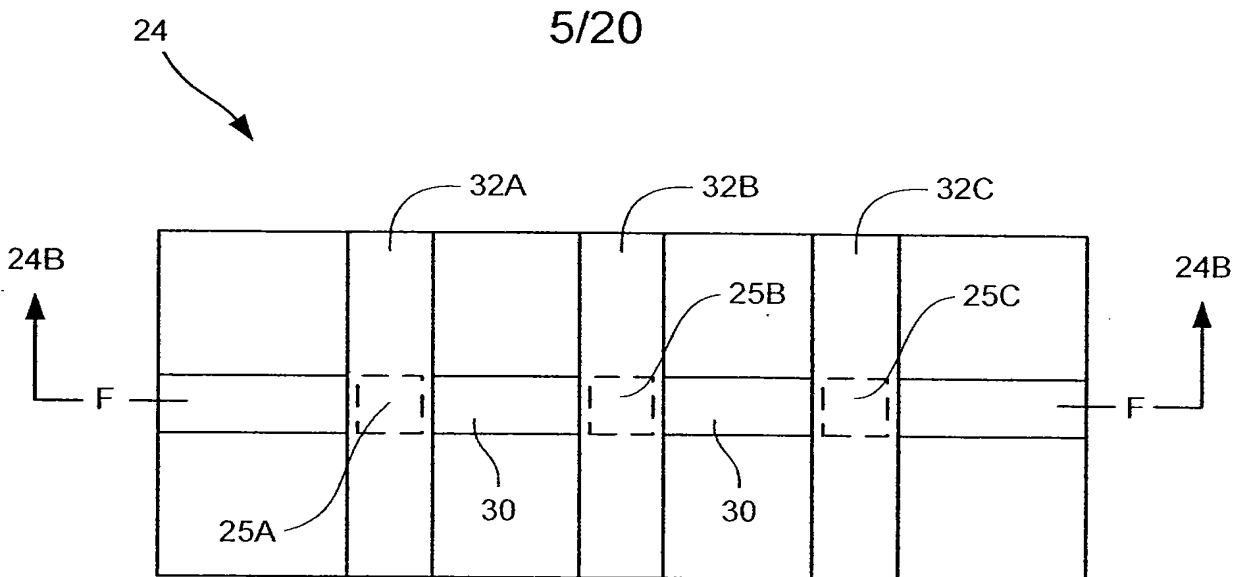


FIG. 5A

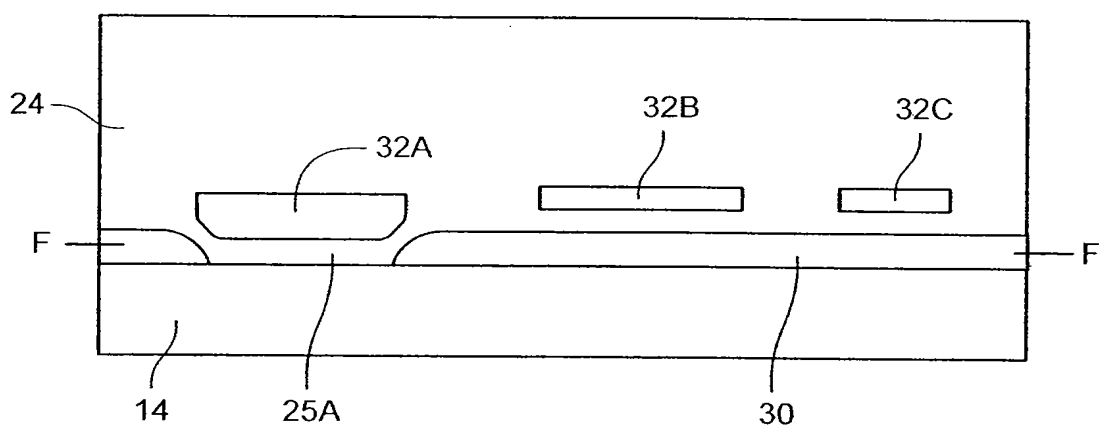


FIG. 5B

6/20

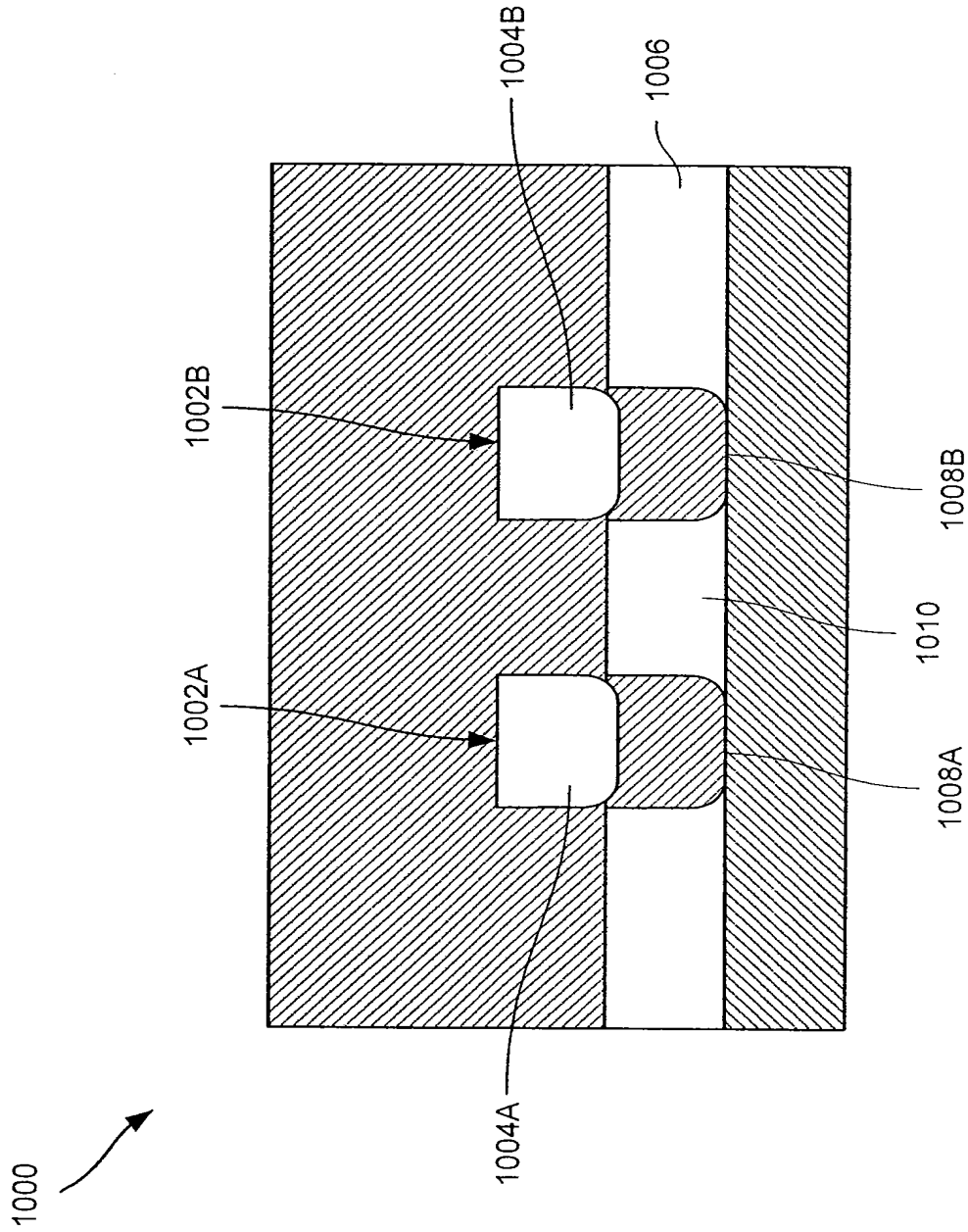


FIG. 6

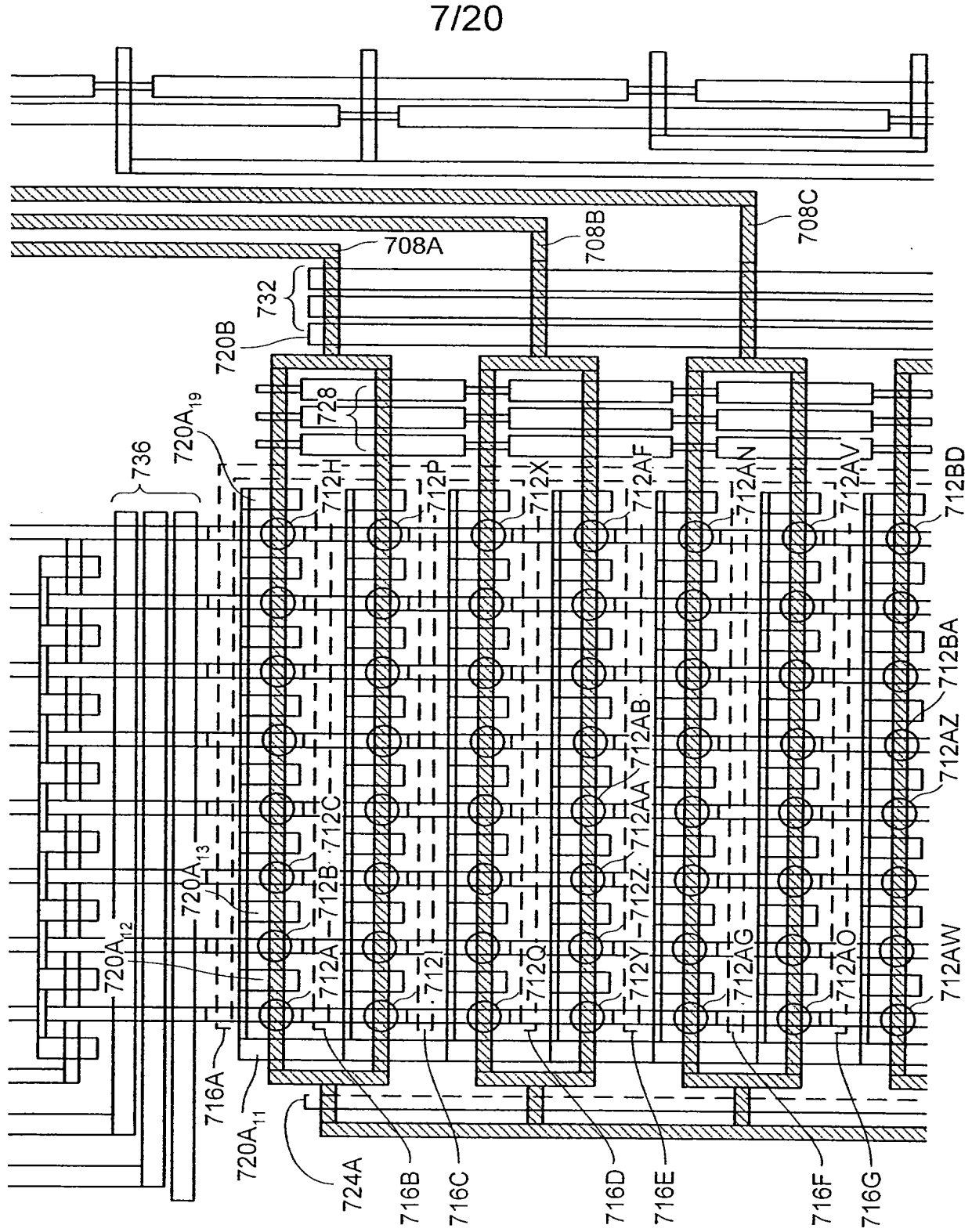


FIG. 7A

8/20

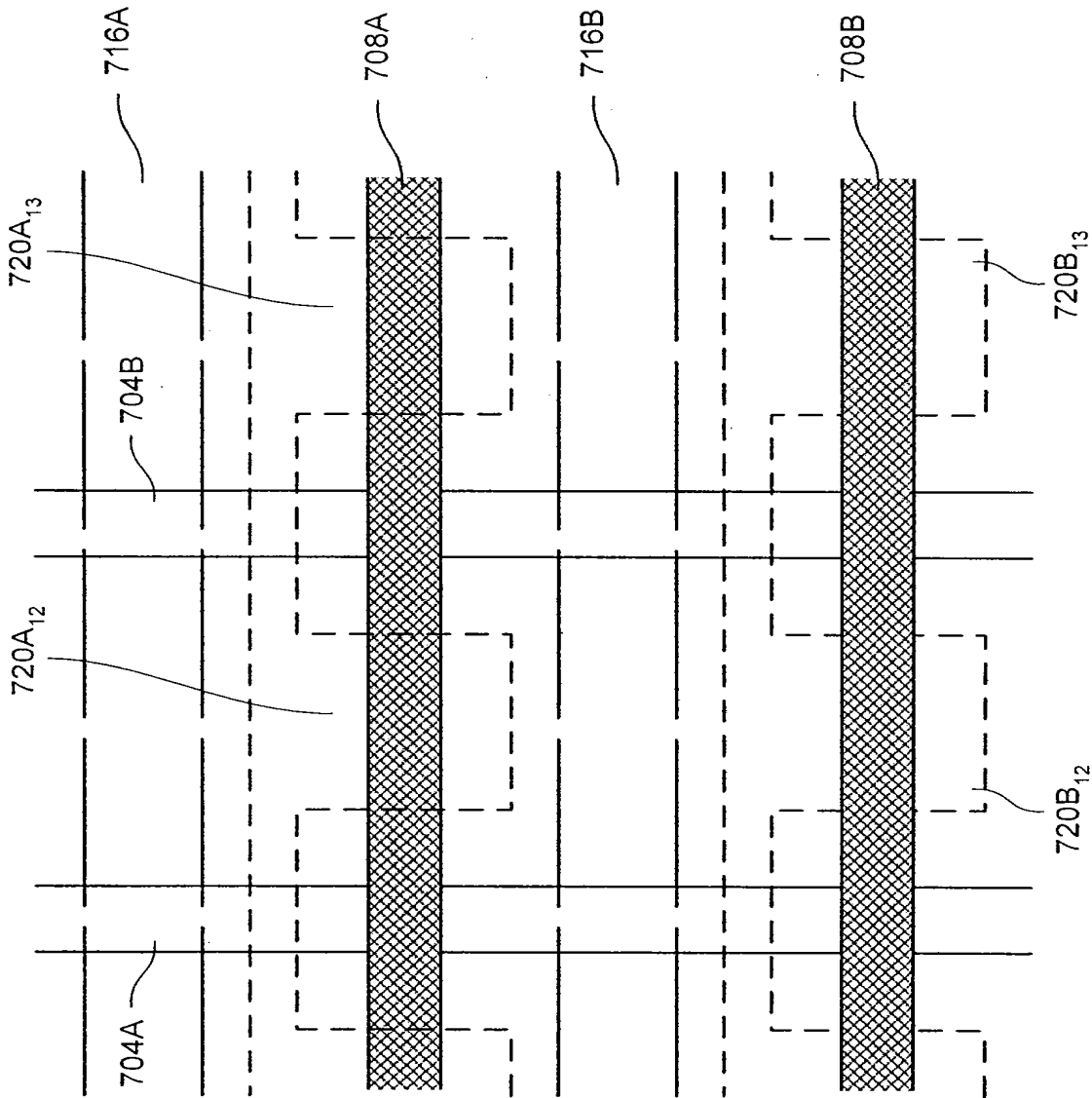


FIG. 7B

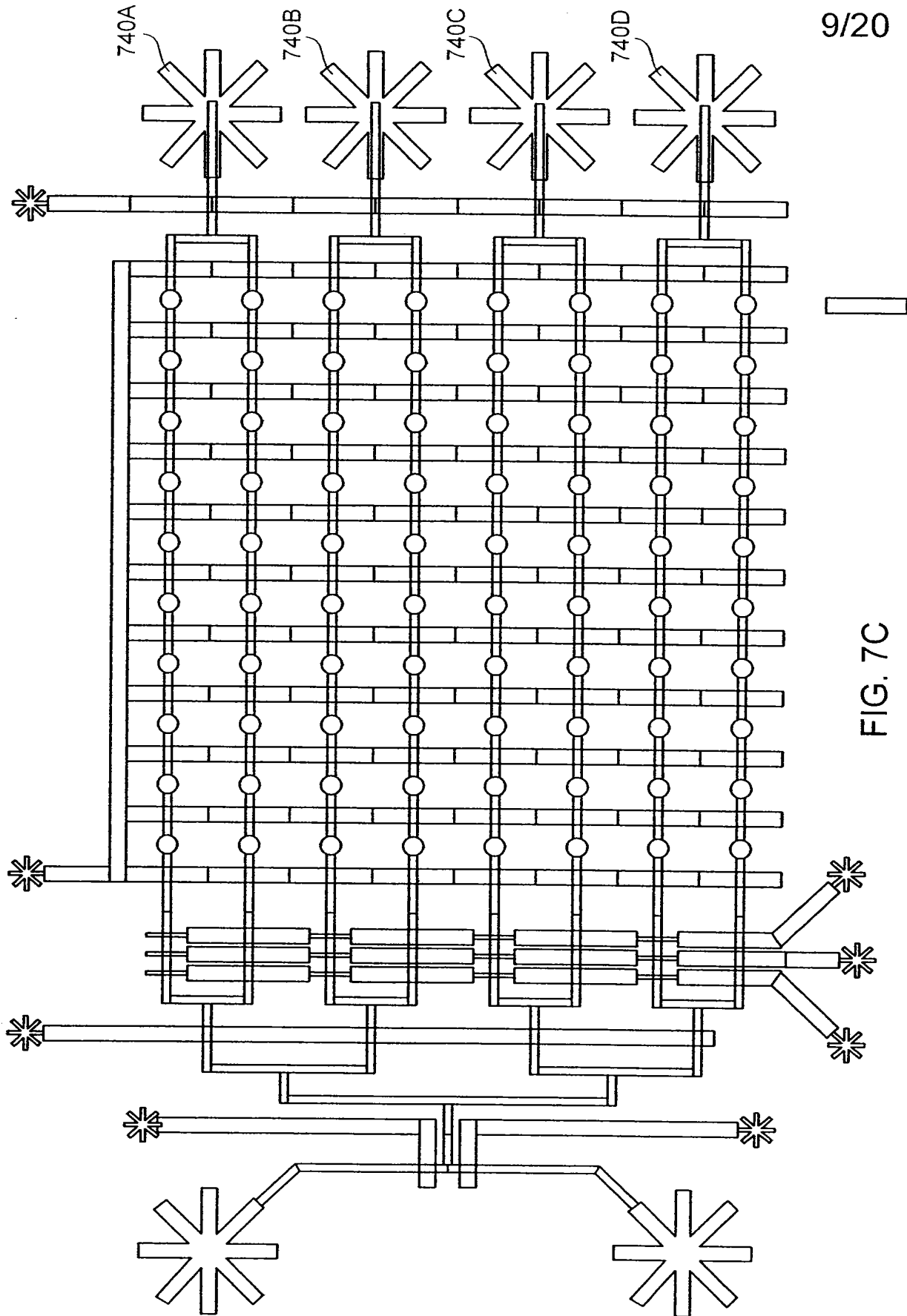


FIG. 7C

10/20

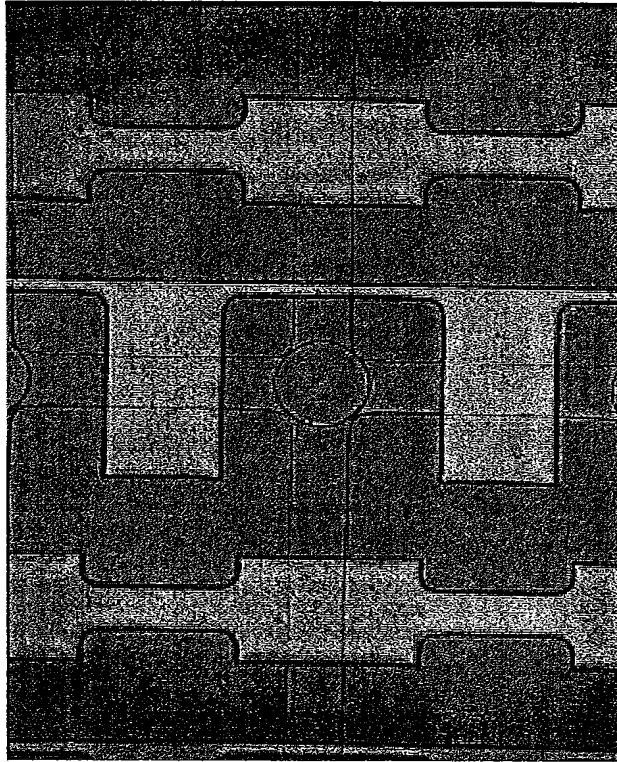


FIG. 7E

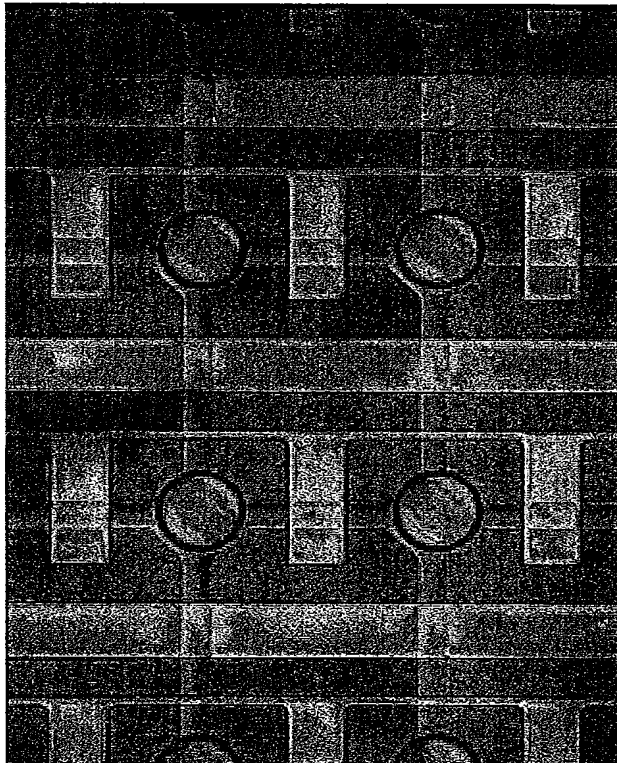
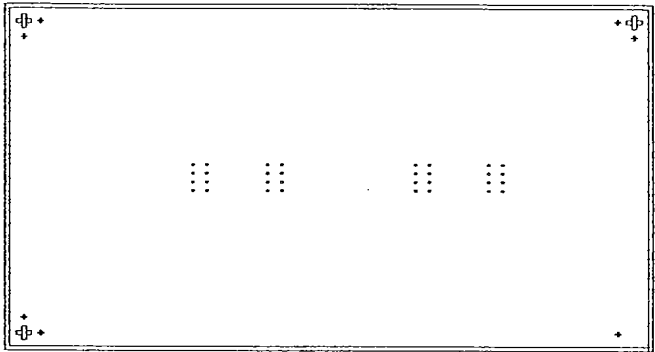


FIG. 7D

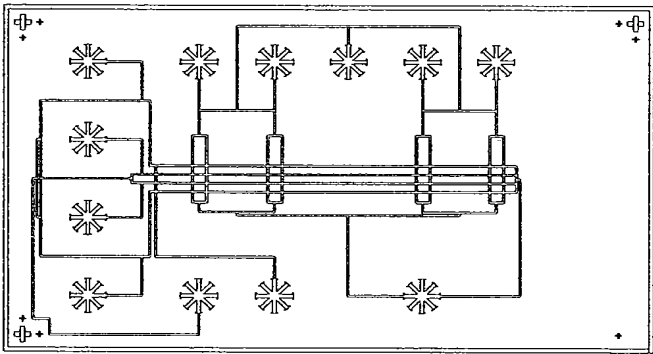
11/20

812



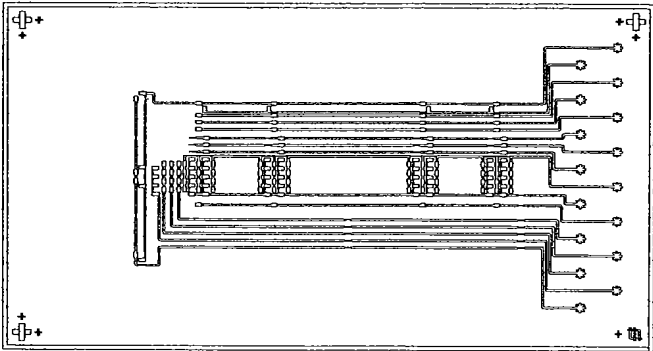
Layer 3
VIA

804



Layer 2
FLUID

808

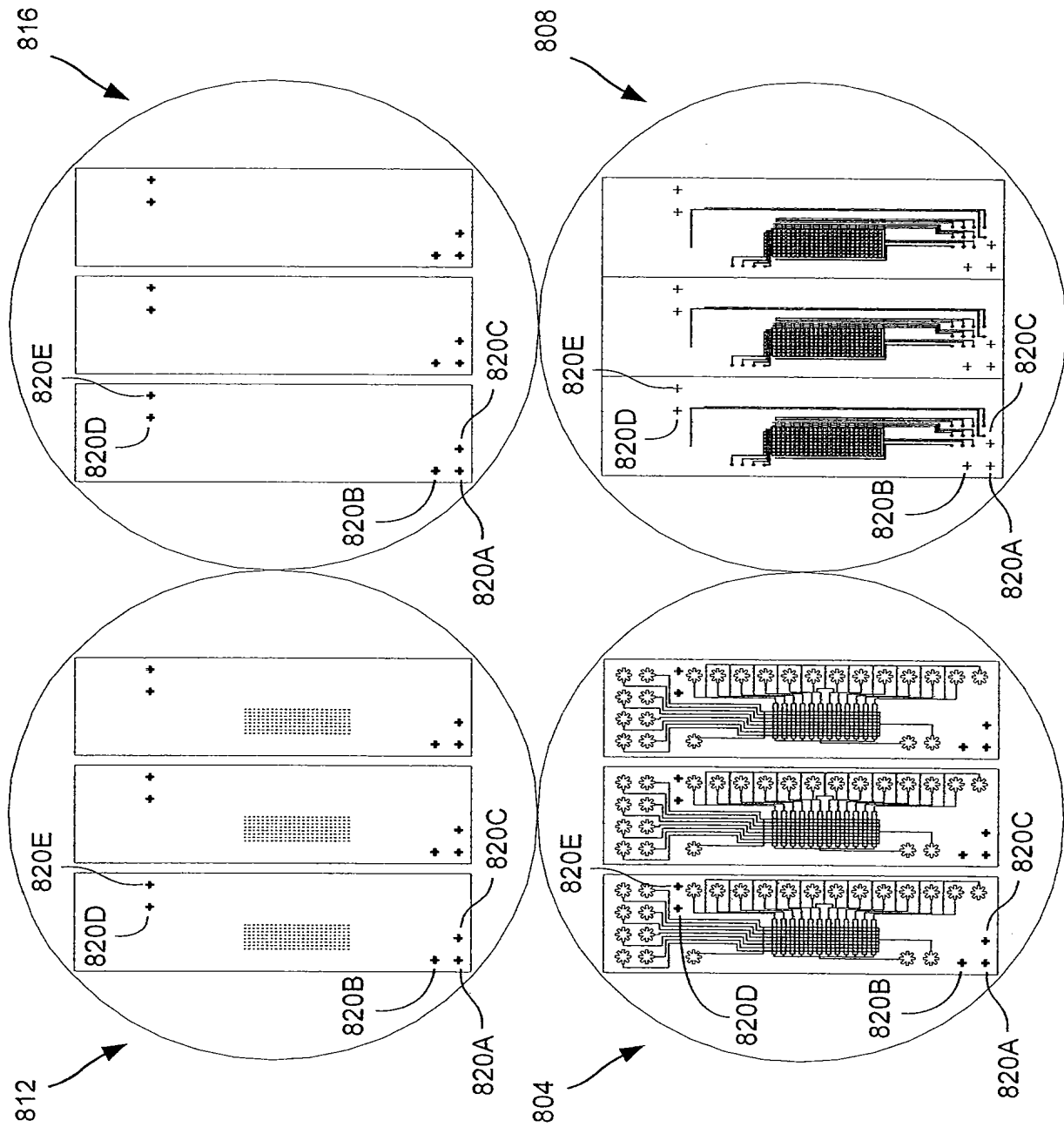


Layer 1
CONTROL

FIG. 8A

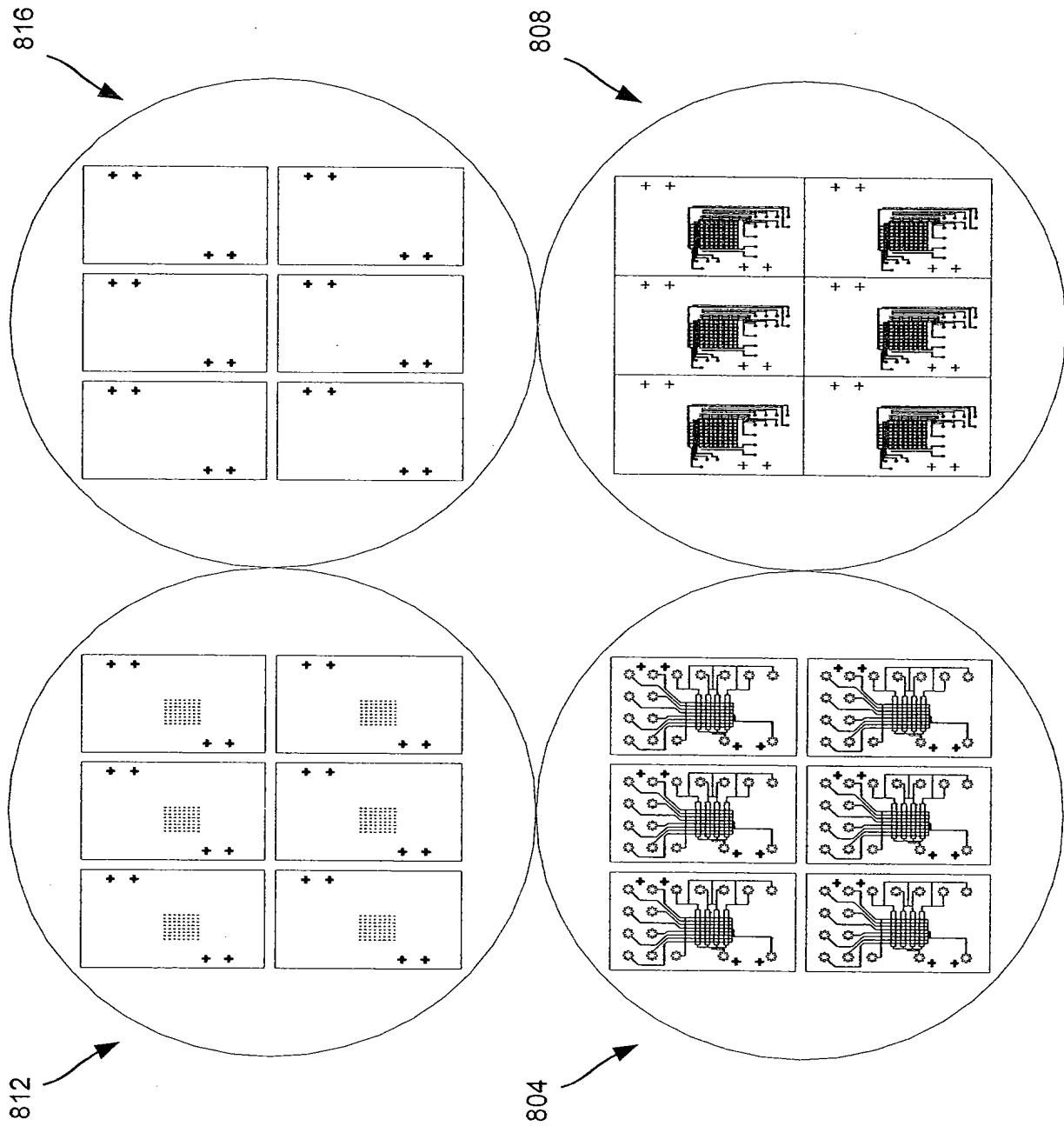
12/20

FIG. 8B



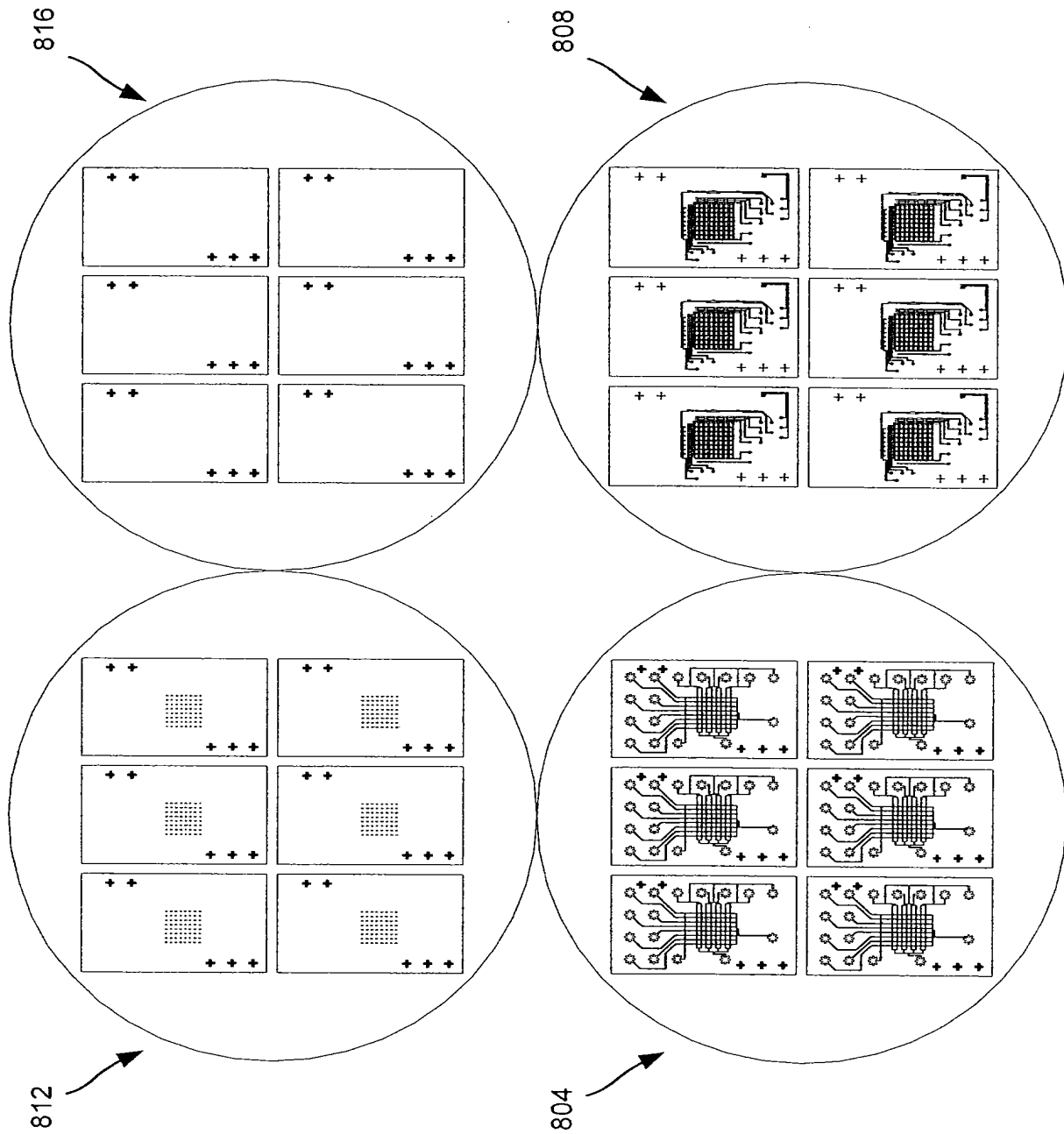
13/20

FIG. 8C



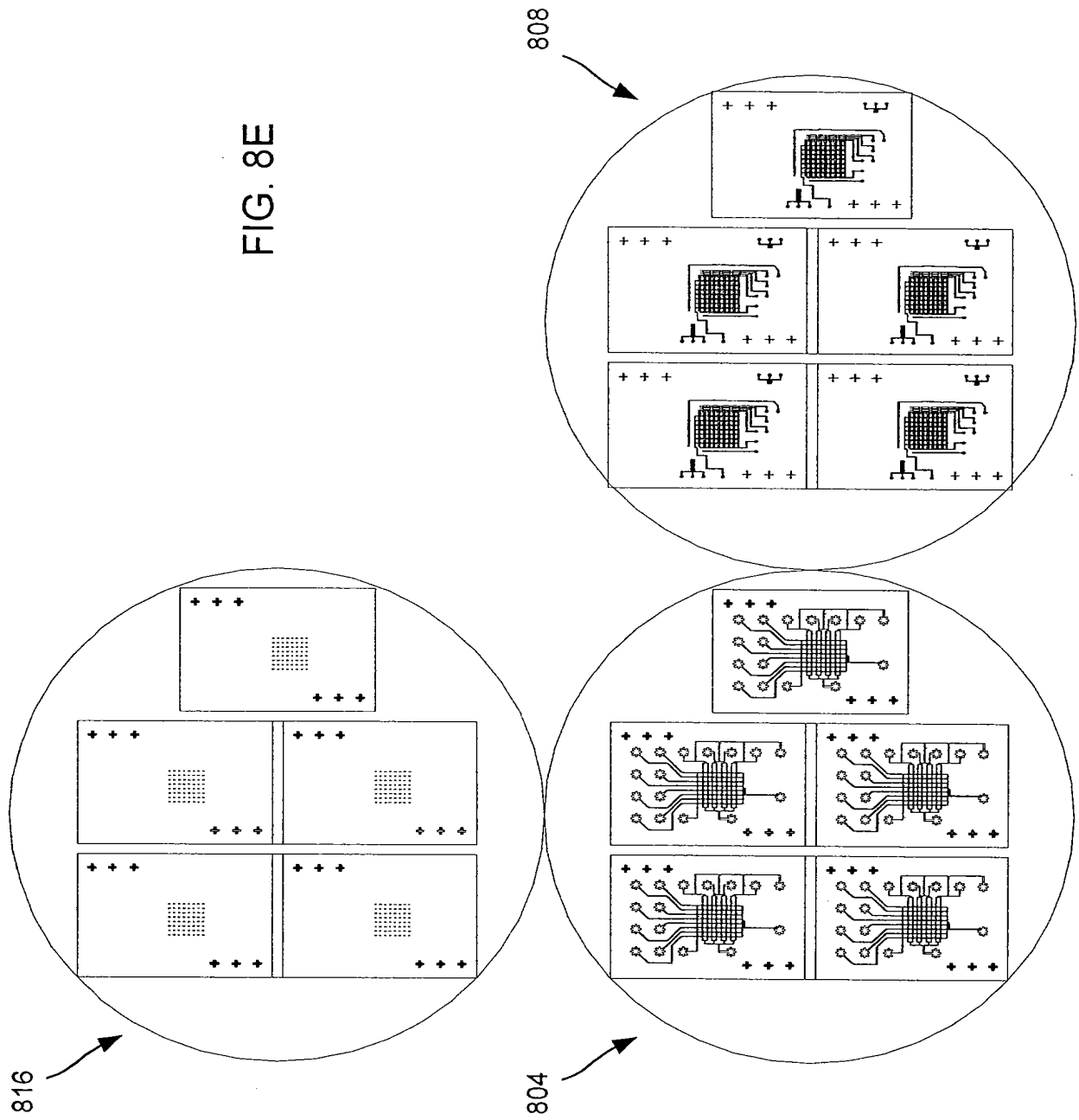
14/20

FIG. 8D



15/20

FIG. 8E



16/20

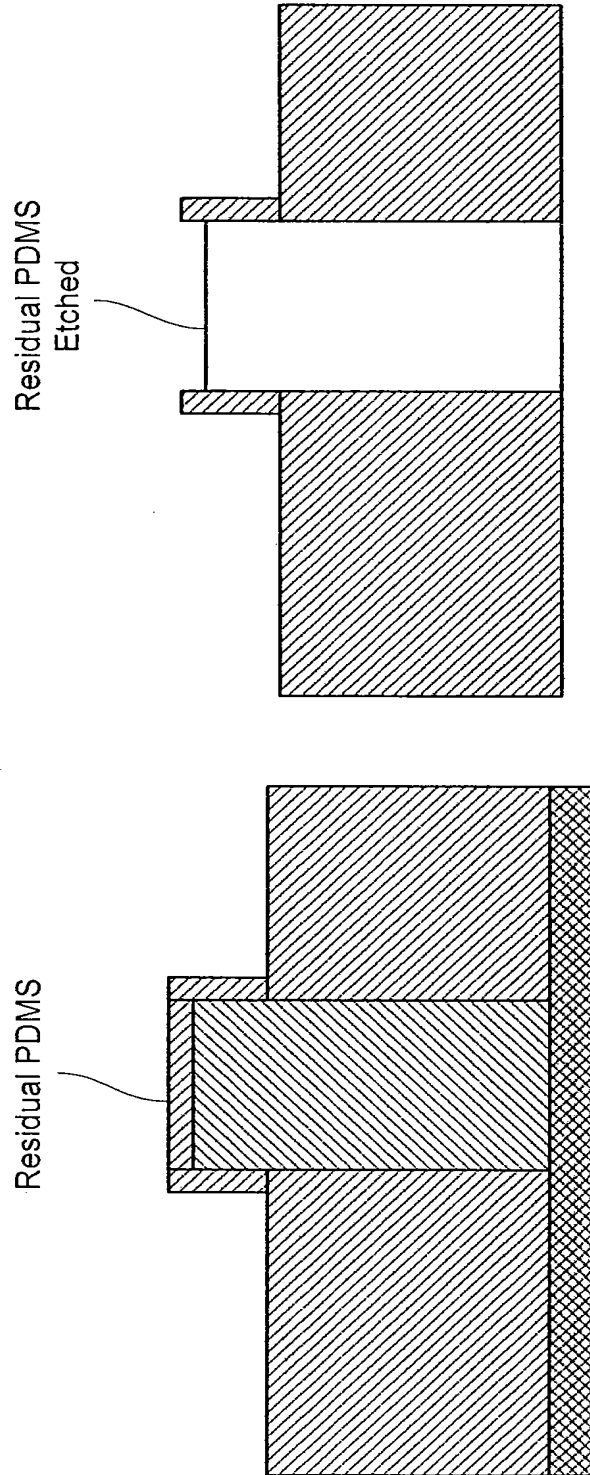


FIG. 9

17/20

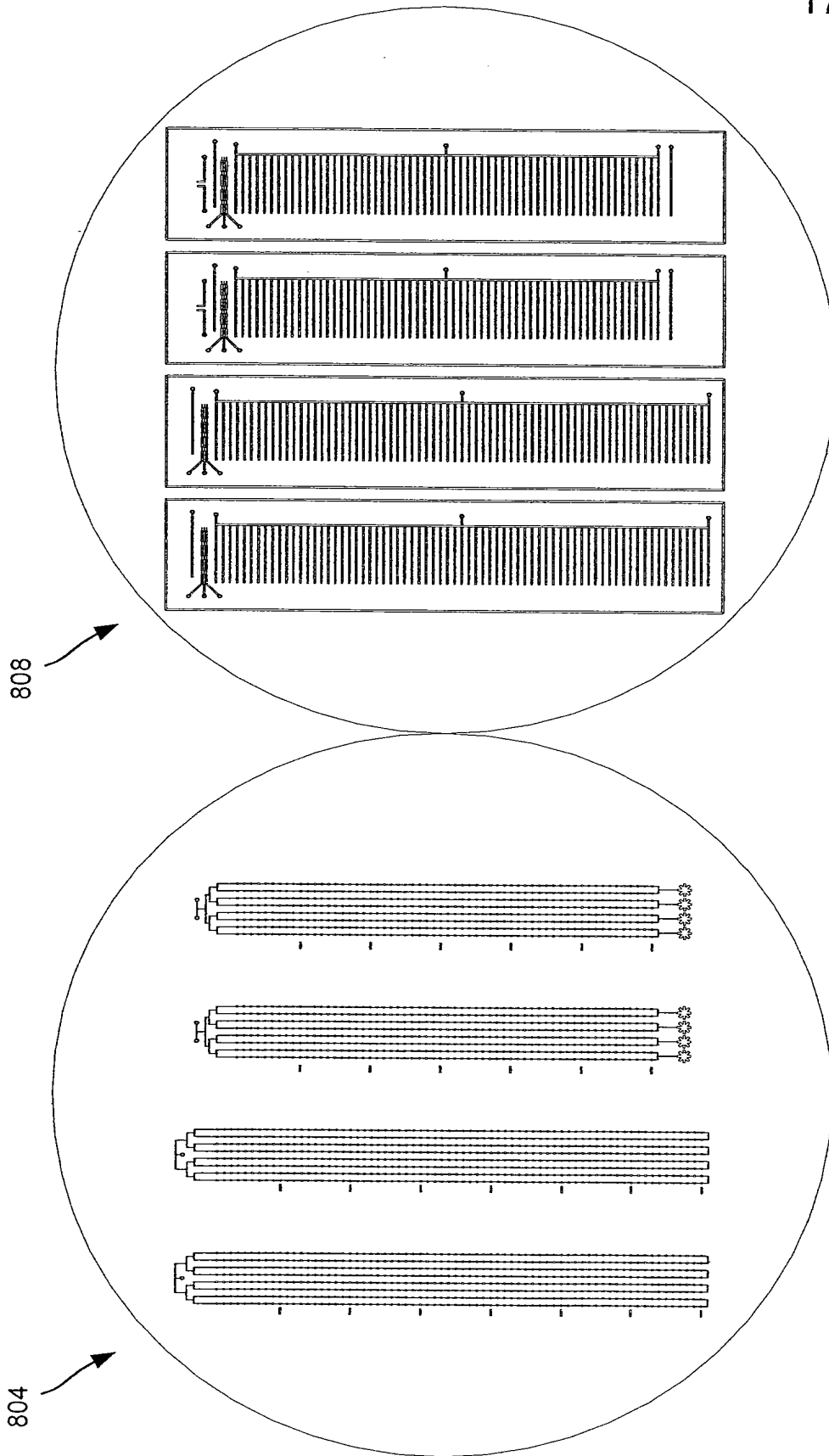


FIG. 10A

18/20

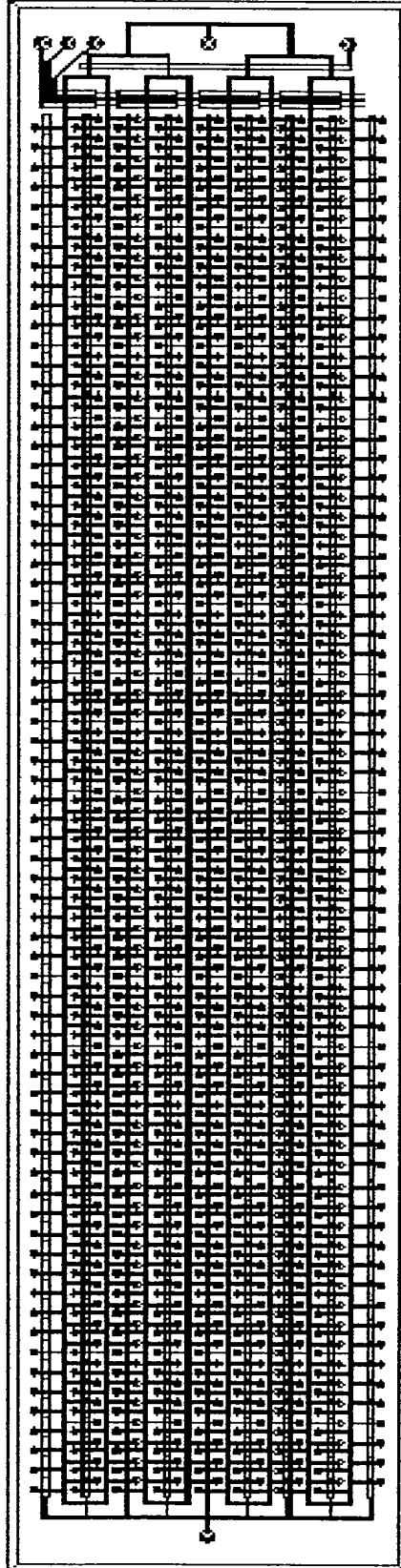
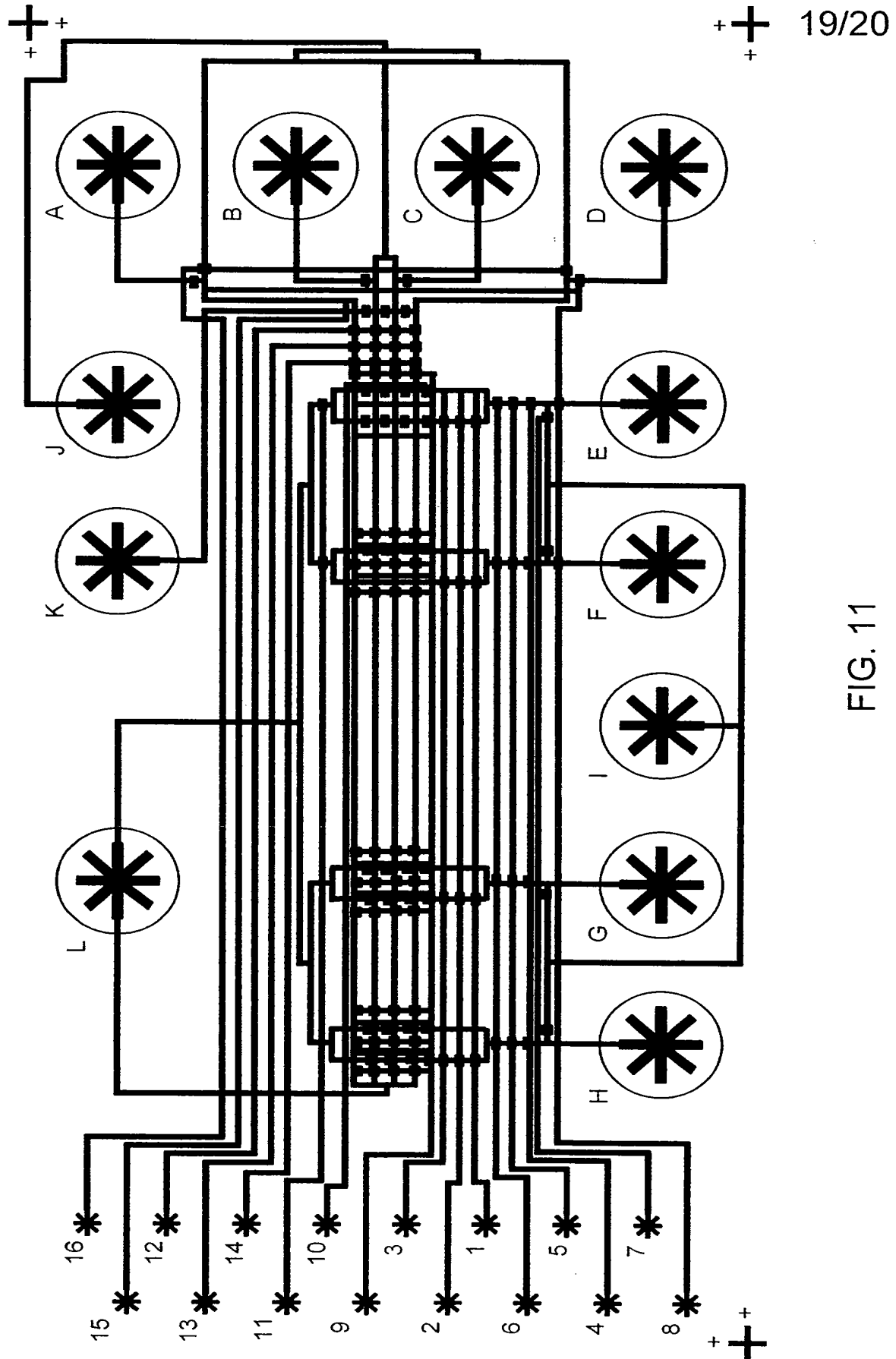


FIG. 10B



20/20

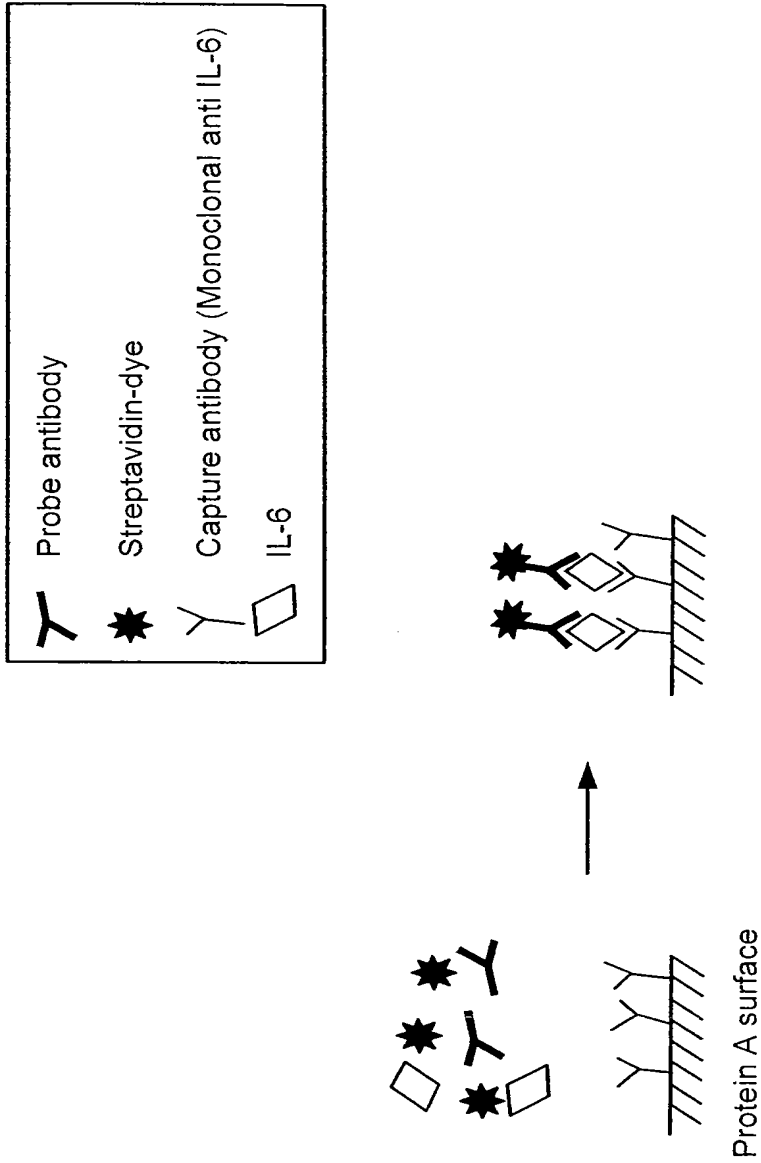


FIG. 12